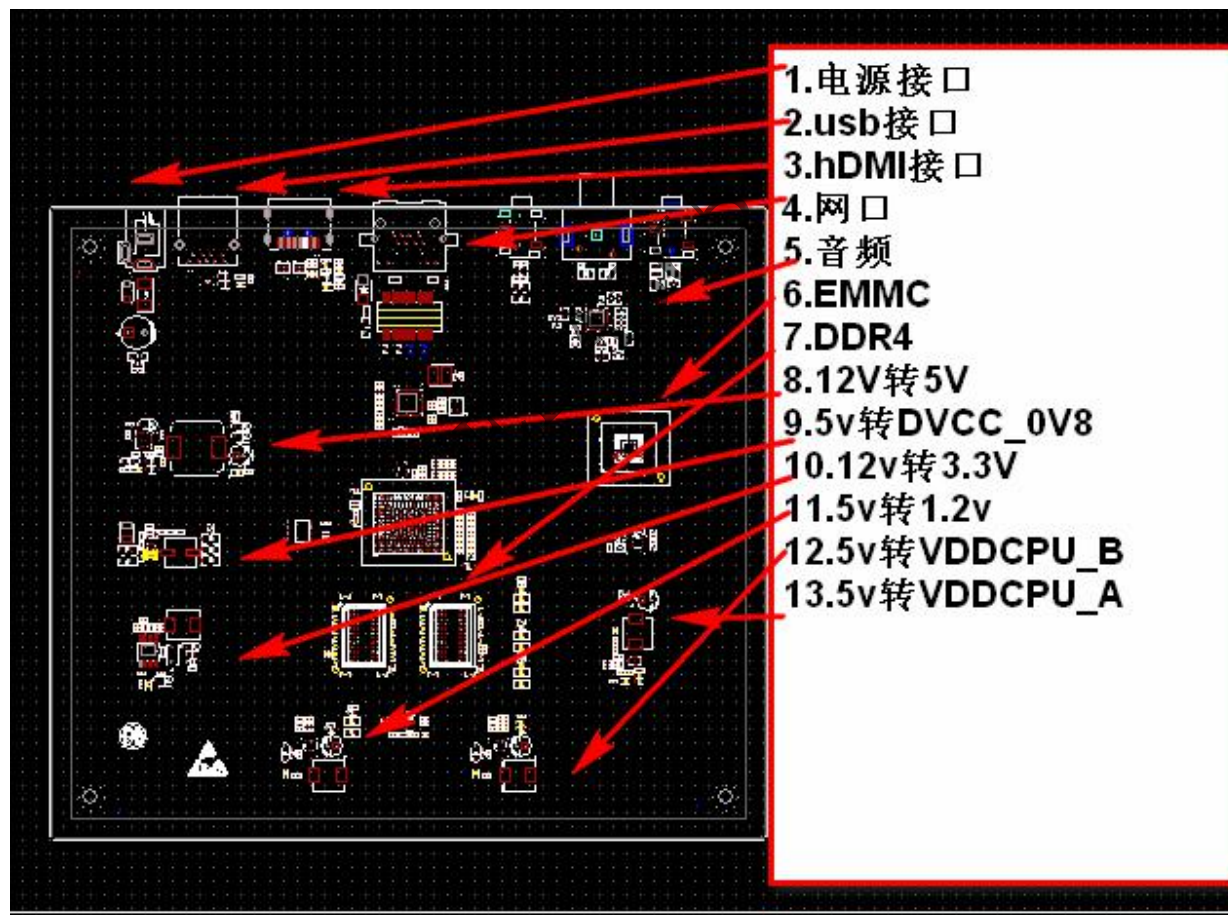


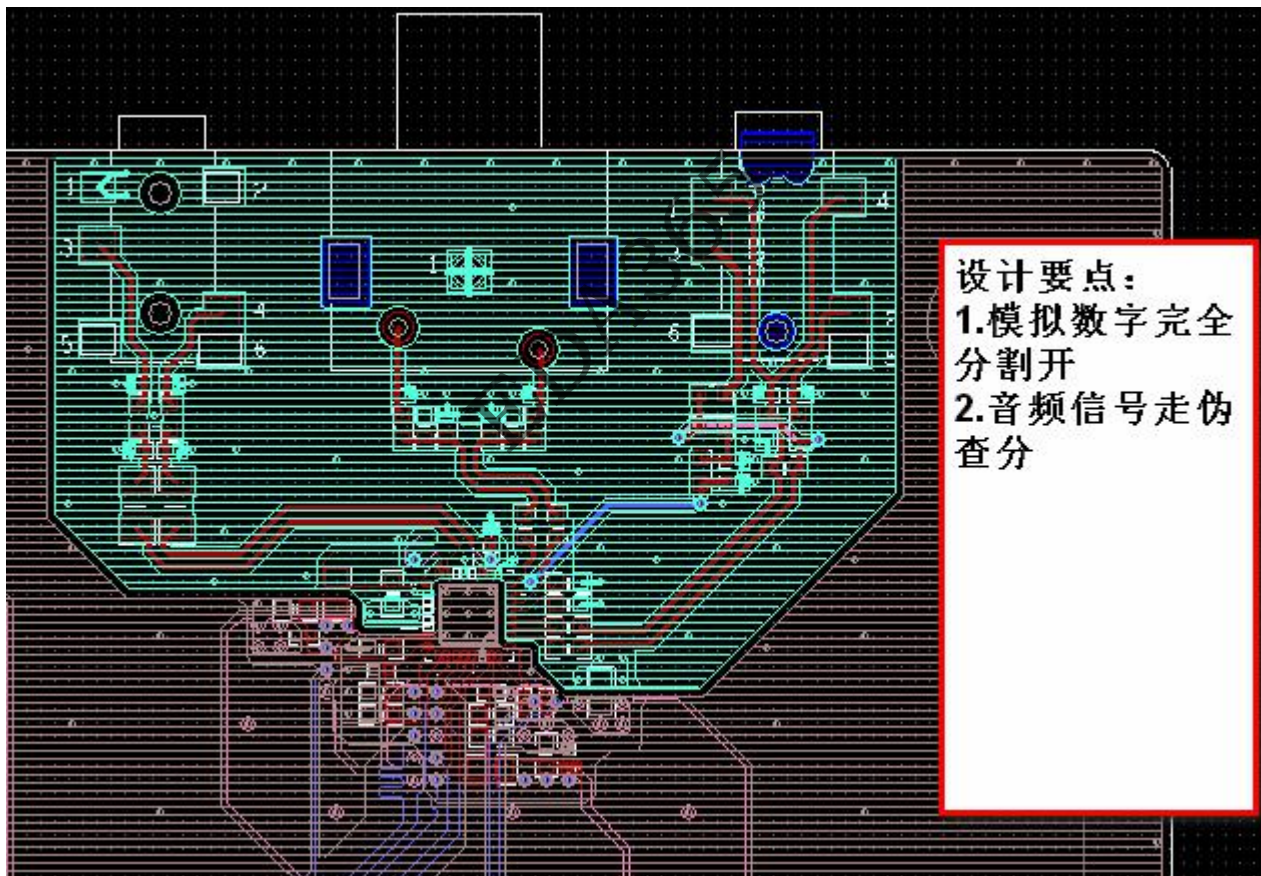
Competition_Board案例 例分析

EDA365

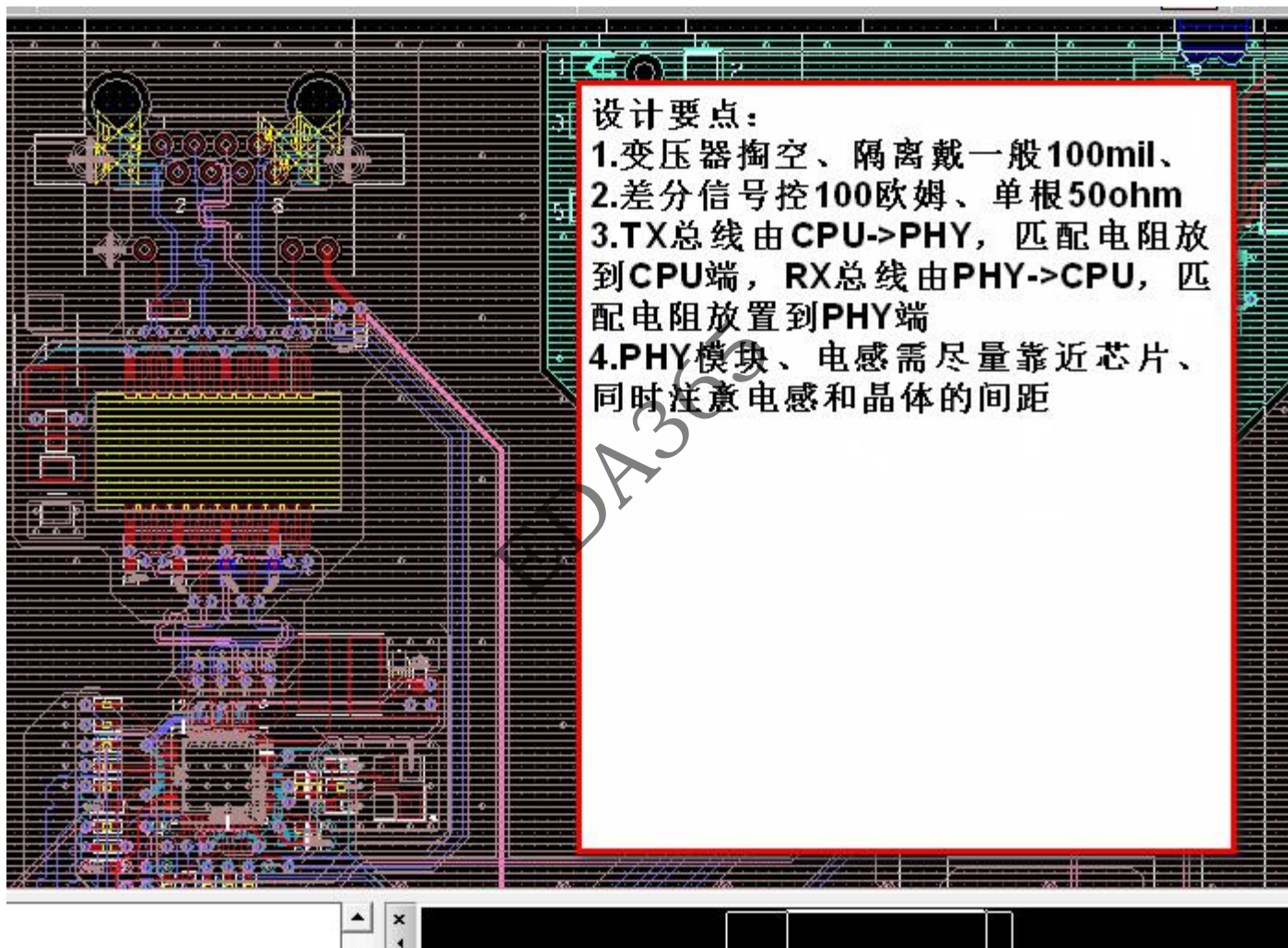
模块布局



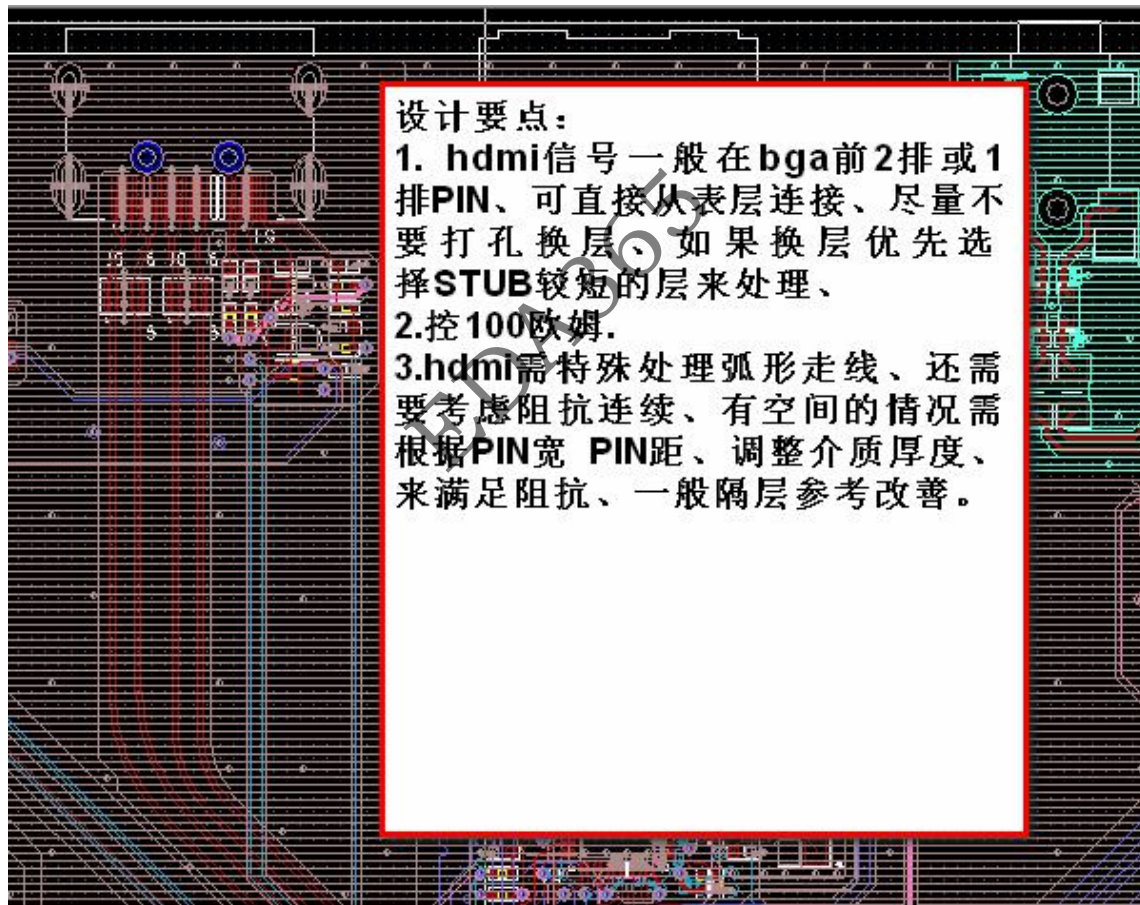
音频模块



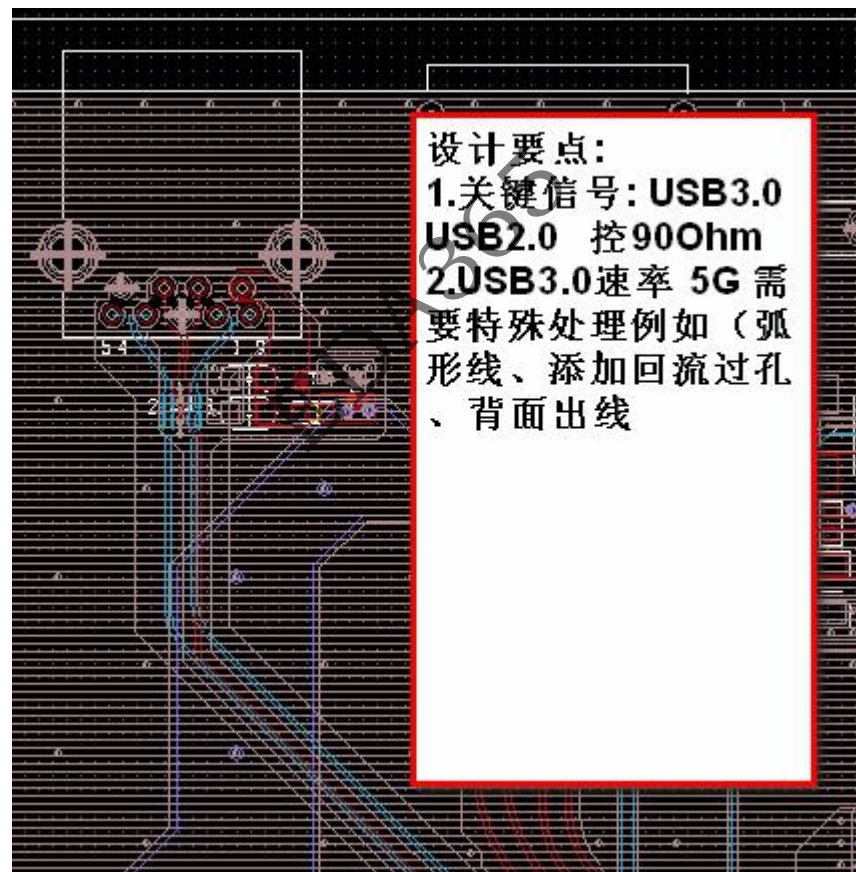
以太网模块



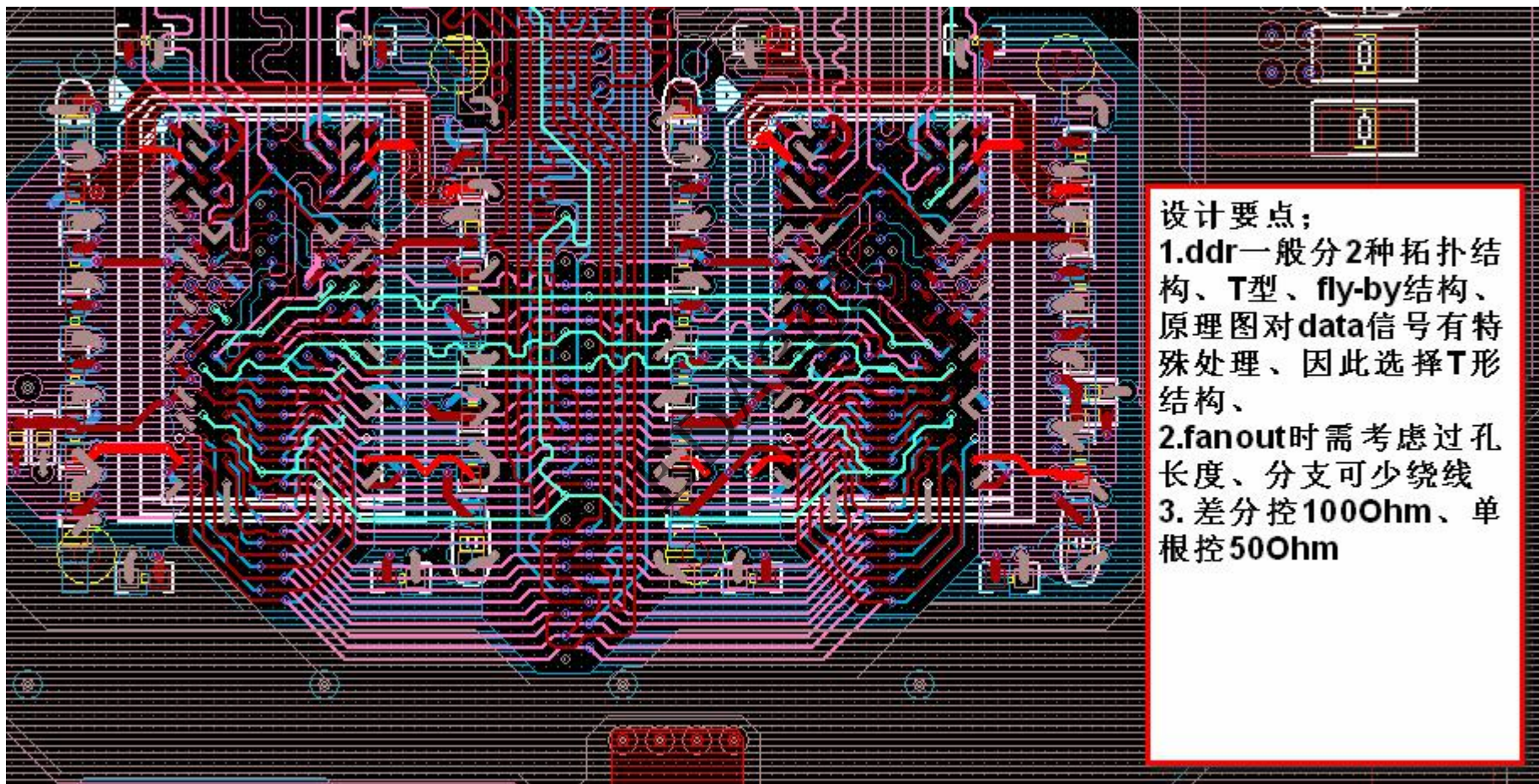
HDMI



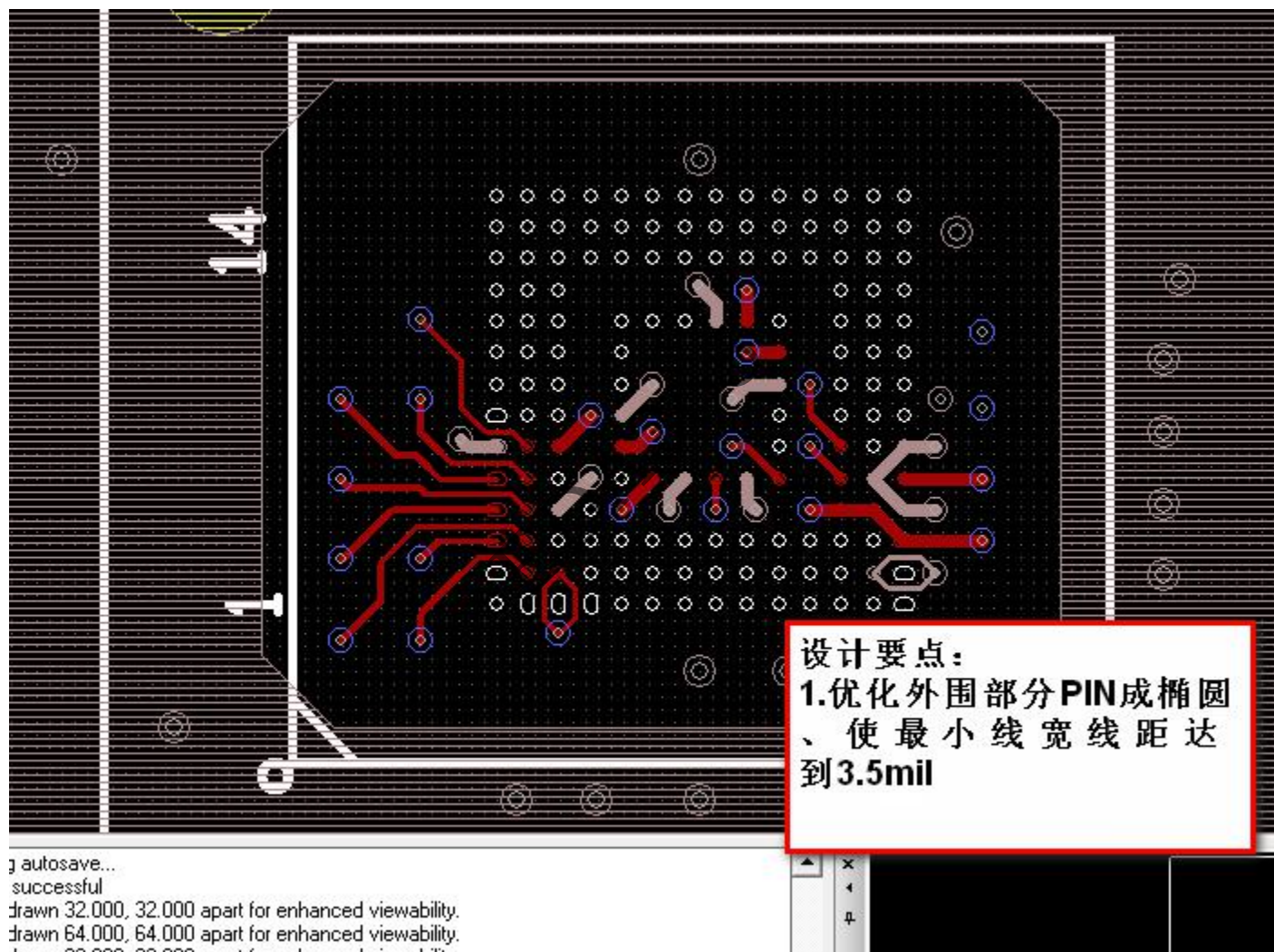
USB



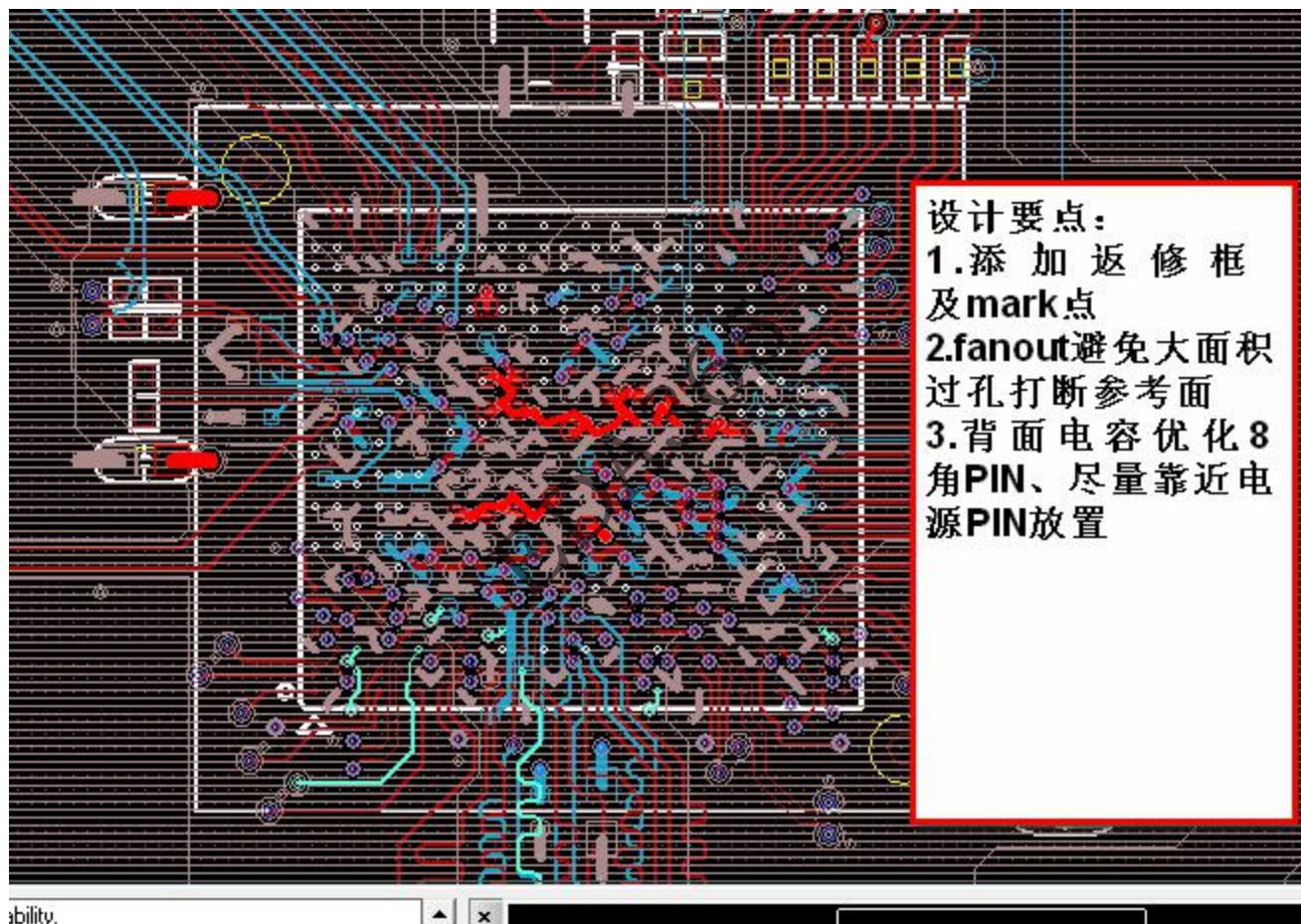
DDR4模块



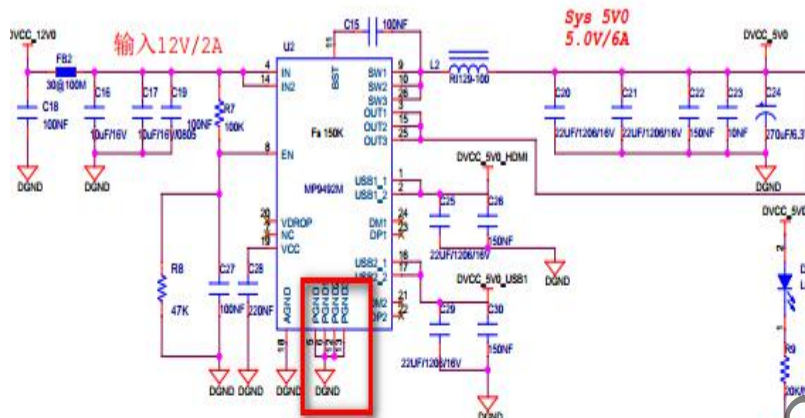
EMMC



主芯片

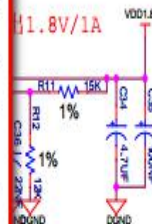
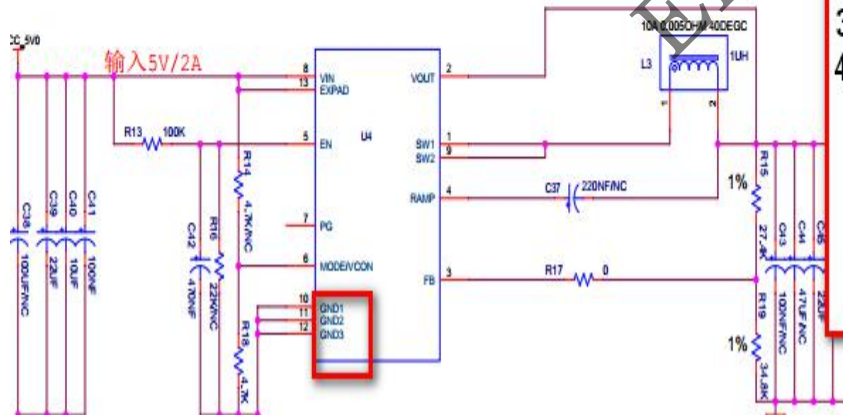


电源模块



设计要点:

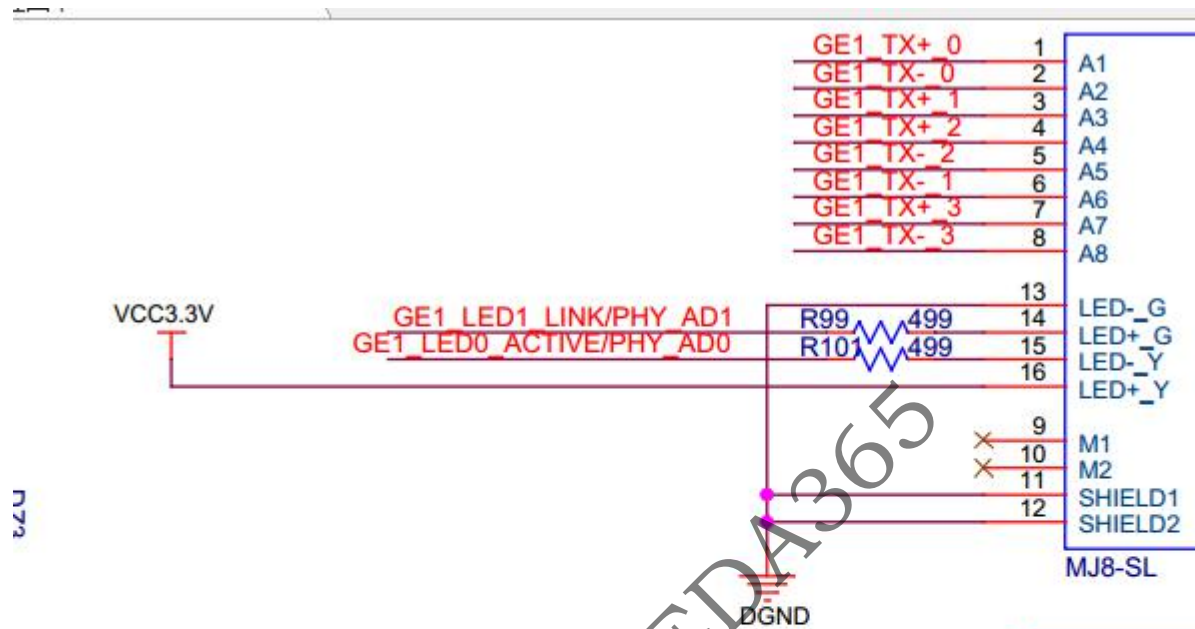
- 1.开关电源、输入输出MOS管电感都需要紧凑的布局、缩短间距、将输入输出的波纹控制在较短的线路中、反馈组容靠近IC)、
- 2.输入输出过孔需要预留余量、功率线尽量粗短
- 3.反馈信号一般15-20mil、
- 4.注意芯片内部属性pgnd 和gnd



问题反馈--优化器件

[illegible]

问题反馈—原图图建议



RJ45 3.3v电源PIN未添加电容、已在芯片端拿取1颗放置过来

问题反馈—网络命名错误

